

09,751,235.

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,759,112 B2
DATED : July 6, 2004
INVENTOR(S) : Wong

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6.

Line 22, insert

-- a substrate;

a layer of material disposed over said substrate; --

Line 22, before "substrate", delete "a" and insert -- said --.

Line 23, after "exposed", insert -- by an etch to remove an overlying portion of said layer of material --.

Line 26, after "below", delete "a" and insert -- said --.

Line 27, after "centerline", insert --, said second pair of features and said first pair of features being coplanar --.

Line 42, after "correct", delete "a" and insert -- said --.

Line 43, after "predict", delete "a" and insert -- said --.

Line 47, after "claim", delete "6" and insert -- 1 --.

Line 49, delete "and covered with transparent material".

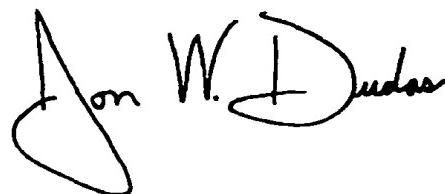
Lines 50-51, delete "A semiconductor structure of claim 7 wherein said third pair of features comprises said transparent material" and insert -- A semiconductor structure of claim 1 wherein said layer of material is transparent --.

Line 52, delete "a first".

Line 54, after "claim", delete "9" and insert -- 1 --.

Signed and Sealed this

Seventh Day of December, 2004



JON W. DUDAS
Director of the United States Patent and Trademark Office